Dr. Yu Bai

Associate Professor
Director of Intelligent Computing Research Lab
University of California, Irvine
Electrical and Computer Engineering Department
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Vice President, AI committee of Sino-American Biotechnology and Pharmaceutical Professional Association Vice President, Chinese Association for Science and Technology in the USA

FUNDING

- Industry-Academic Research Grant,: "High Efficient AI Algorithm for X-ray Images", June. 1, 2023-July. 30, 2025, \$20,000.
- Industry-Academic Research Grant,: "AI technology in health products", Oct. 1,2021-Sep. 30,2024, \$150,000.
- Industry-Academic Research Grant,: "Efficient image reflection removal using perceptual deep learning", Nov. 30,2020-May 20,2021, \$20,000.
- Army Research Office (#W911NF2010174): Edge-Based Machine Intelligence Architecture for In-Situ Video Processing using Binarized Neutral Networks" June 10, 2020-June 9, 2023, "\$418,657
- National Science Foundation (NSF#1832536): Building Capacity: Advancing Student Success in Undergraduate Engineering and Computer Science (ASSURE-US), October 1, 2018-September 30, 2023, "\$1,496,967
- 2021-2022 Junior/Senior Intramural Grant: Harvesting Social Media Data to Quantify Effects of Anti-immigrant Policy and Rhetoric on Health Outcomes
- Research, Scholarship and Creative Activity (RSCA) Award: Discovering and Assessing Data Bias in Social Media through Smart Label-relational Mention-context Analysis" \$15,000, Jan. 1, 2020-Dec. 30, 2020
- ECS Incentive Grant: Harvesting Social Media Data to Detect Medical Misinformation and Predict Impact on Health Behaviors due to COVID-19. \$16,000, Jan. 13, 2021-May. 13, 2021
- Intel University Program: Hardware accelerated Binarilized Deep Learning," \$8000
- ECS Incentive Grant: Hardware Assisted Cooperative Infrastructure Defense Framework." \$16,000 Jan. 12, 2019-May. 13, 2019

PUBLICATIONS

Book Chapter

- Gofman, Sinjini Mitra, **Yu Bai**, and Yoonsuk Choi, "Security, Privacy, and Usability Challenges in Selfie Biometrics." In Selfie Biometrics, pp. 313-353. Springer, Cham, 2019.
- X.Tang, X. Xu, Y. Bai, "Computer Essential," Tsinghua University Press, ISBN 978730243418.

Journal Papers

- [Elsevier Internet of Things'23, IF=5.71] Liu, Mingshuo, Miao Yin, Kevin Han, Ronald F. DeMara, Bo Yuan, and Yu Bai. "Algorithm and hardware co-design co-optimization framework for LSTM accelerator using quantized fully decomposed tensor train." Internet of Things (2023): 100680.
- [Micromachines'22 (Impact Factor=3.523)] Liu, Mingshuo, Shiyi Luo, Kevin Han, Ronald F. DeMara, and Yu Bai. "Autonomous Binarized Focal Loss Enhanced Model Compression Design Using Tensor Train Decomposition." Micromachines 13, no. 10 (2022): 1738.
- [IEEE TED'22 (Impact Factor=2.917)] M. Liu, P. Borulkar, M. Hossain, R. F. Demara and Y. Bai*, "Spin-Orbit Torque Neuromorphic Fabrics for Low-Leakage Reconfigurable In-Memory Computation," in IEEE Transactions on Electron Devices, vol. 69, no. 4, pp. 1727-1735, April 2022, doi: 10.1109/TED.2021.3140040.
- [Springer JPUC'21 (Impact Factor=3.006)] Xia, Y., Qu, S., Goudos, S., Bai, Y., & Wan, S. (2021). Multi-object tracking by mutual supervision of CNN and particle filter. Personal and Ubiquitous Computing, 25(6), 979-988.

- [Springer JPUC'20 (Impact Factor=2.395)] Jianhong Yang, Y. Bai*. "Centered Convolutional Deep Boltzmann Machine for 2D Shape Modeling." Personal and Ubiquitous Computing, Springer. Accepted.
- [CAI'20 (Impact Factor=0.542)] Yanhui Guo, Yu Wang, Y. Bai*, "DEEP LSTM WITH GUIDED FILTER FORHY-PERSPECTRAL IMAGE CLASSIFICATION." COMPUTING AND INFORMATICS, Accepted
- [Springer JPUC'19 (Impact Factor=2.395)] Xia, Yu, Shiru Qu, Sotirios Goudos, Y. Bai, and Shaohua Wan. "Multi-object tracking by mutual supervision of CNN and particle filter." Personal and Ubiquitous Computing (2019): 1-10. Springer.
- [IEEE TETC'19 (IF=4.989)] A. Samiee, P. Borulkar, R. F. DeMara, P. Zhao and Y. Bai*, "Low-Energy Acceleration of Binarized Convolutional Neural Networks using a Spin Hall Effect based Logic-in-Memory Architecture," in IEEE Transactions on Emerging Topics in Computing. doi: 10.1109/TETC.2019.2915589
- [IEEE ACCESS'19 (IF=3.55)] Ashkan Samiee, Yunchuan Sun, Ronald DeMara, Yoonsuk Choi, and Yu Bai*, "Energy Efficient Mobile Service Computing with Differential Spintronic-C-elements: A Logic-in-Memory Asynchronous Computing Paradigm" 10.1109/ACCESS.2019.2911098
- [Springer JWCN'19 IF=2.407] Yanhui Guo, Xijie Yin, Xuechen Zhao, Dongxin Yang, and Yu Bai*, "Hyperspectral Image Classification with SVM and Guided Filter," EURASIP Journal on Wireless Communications and Networking, Accepted.
- [IEEE JSTARS'19 IF=2.777] Yanhui Guo, Han Cao, Jianjun Bai, and Yu Bai*, "High Efficient Deep Feature Extraction and Classification of Spectral-Spatial Hyperspectral Image using Cross Domain Convolutional Neural Networks," IEEE Journal of Selected Topics in Applied Earth Observations and Remote Sensing, Accepted.
- [IEEE ACCESS'18 IF=3.55] Yanhui Guo, Cao Han, Yunchuan Sun, and Yu Bai*, "Spectral-Spatial Hyperspectral Image Classification with K-Nearest Neighbor and Guided filter," IEEE Access, vol. PP, no. 99, pp. 1-1. doi: 10.1109/ACCESS.2018.2820043.
- [IEEE ACCESS'18 IF=3.55] Yanhui Guo, Siming Han, Chuanhe Shen, Ying Li, Xijie Yin and Yu Bai*, "An Adaptive SVR for High-Frequency Stock Price Forecasting," in IEEE Access, vol. PP, no. 99, pp. 1-1. doi: 10.1109/ACCESS.2018.2806180
- [IEEE TMSCS'17] Yu Bai, D. Fan, and M. Lin, "Stochastic-Based Synapse and Soft-Limiting Neuron with Spintronic Devices for Low Power and Robust Artificial Neural Networks," in IEEE Transactions on Multi-Scale Computing Systems, 10.1109/TMSCS.2017.2787109.
- [IEEE TC'17 IF=2.916] Yu Bai, R. F. DeMara, J. Di and M. Lin, "Clockless Spintronic Logic: A Robust and Ultra-Low Power Computing Paradigm," in IEEE Transactions on Computers, vol. PP, no. 99, pp. 1-1. doi: 10.1109/TC.2017.2776139
- [IEEE TETC'16 IF=3.626] M. Alawad, Yu Bai, R. DeMara and M. Lin, "Robust Large-Scale Convolution through Stochastic-Based Processing Without Multipliers," in IEEE Transactions on Emerging Topics in Computing, vol. PP, no. 99, pp. 1-1. doi: 10.1109/TETC.2016.2601220

Conference Papers

- [IEEE ISQED'23] Ma, Xiaotian, Kevin Han, Ronald F. DeMara, and Yu Bai*. "Locality-sensing Fast Neural Network (LFNN): An Efficient Neural Network Acceleration Framework via Locality Sensing for Real-time Videos Queries." In 2023 IEEE The 24th International Symposium on Quality Electronic Design.
- [IEEE SOCC'22] Ma, Xiaotian, Kevin Han, Yucheng Yang, Ronald F. DeMara, and Yu Bai*. "Hardware Oriented Strip-wise Optimization (HOSO) Framework for Efficient Deep Neural Network." In 2022 IEEE 35th International System-on-Chip Conference (SOCC), pp. 1-6. IEEE, 2022.
- [IEEE ASEE'22] Kurwadkar, Sudarshan, Salvador Mayoral, Antoinette Linton, Doina Bein, Paulina Reina, Jidong Huang, Yu Bai, Ariana Vasquez, and Kirk Vandersall. "Advancing Student Success Through Integrated Sociocultural and Academic Intervention Strategies." In 2022 ASEE Annual Conference and Exposition. 2022.
- [DAC'21] Liu, Mingshuo, Miao Yin, Kevin Han, Shiyi Luo, Mingju Liu, Ronald F. DeMara, Bo Yuan, and Yu Bai*. "Algorithm and Hardware Co-Design Co-Optimization Framework for LSTM Accelerator using Fully Decomposed Tensor Train." DAC (Work-in-Progress) (2021).
- [ASEE'21] Bein, Doina, Jidong Huang, Yu Bai*, Sudarshan T. Kurwadkar, and Paulina Reina. "Lessons Learned From the First-Year Enrichment Program for Engineering and Computer Science Students in the ASSURE-US Program." In 2021 ASEE Virtual Annual Conference Content Access. 2021.

- [ASAP'21] Liu, Mingshuo, Shiyi Luo, Kevin Han, Bo Yuan, Ronald F. DeMara, and Y. Bai*. "An Efficient Real-Time Object Detection Framework on Resource-Constricted Hardware Devices via Software and Hardware Co-design." In 2021 IEEE 32nd International Conference on Application-specific Systems, Architectures and Processors (ASAP), pp. 77-84. IEEE, 2021.
- [GLSVLSI'21] Liu, Mingshuo, Kevin Han, Shiyi Luo, Mingze Pan, Mousam Hossain, Bo Yuan, Ronald F. DeMara, and Y. Bai*. "An Efficient Video Prediction Recurrent Network using Focal Loss and Decomposed Tensor Train for Imbalance Dataset." In Proceedings of the 2021 on Great Lakes Symposium on VLSI, pp. 391-396. 2021.
- [ASEE'20] Sudarshan Kurwadkar, Jidong Huang, Doina Bein, Mayoral, Salvador, and Y. Bai*, "Improving STEM Education for Lower-division College Students at HSI by Utilizing Relevant Sociocultural and Academic Experiences: First Year Results from ASSURE-US Project" Annual Conference of American Society for Engineering Education (ASEE2020), accepted
- [ICIT'20] Janelle Estabillo, Derrick Lee, Christopher Ly, Graciela Orozco, Doina Bein, Sudarshan Kurwadkar, Jidong Huang, and Yu Bai. "Using Projects on Clustering and Linear Regression to Develop Basic Research Skills in Freshmen and Sophomore Undergraduate Students." In 17th International Conference on Information Technology–New Generations (ITNG 2020), pp. 379-384. Springer, Cham, 2020.
- [ICIT'20] Ahmed, A., Macias, L., McCune, M., Medina, M., Orozco, G., Bein, D., Kurwadkar, S., Huang, J., Daescu, O., Xu, D. and Bai, Y., 2020. Initiating Research Skills in Undergraduate Students Through Data Science Projects. In 17th International Conference on Information Technology–New Generations (ITNG 2020) (pp. 385-391). Springer, Cham.
- [FICC'19] Rangaswamaiah, Chaitra, Y. Bai*, and Yoonsuk Choi. "Multilevel Data Concealing Technique Using Steganography and Visual Cryptography." In Future of Information and Communication Conference, pp. 739-758. Springer, Cham, 2019.
- [IEEE ICASSP'19] Siyu Liao, Ashkan Samiee, Chunhua Deng, Yu Bai, Bo Yuan, "COMPRESSING DEEP NEURAL NETWORKS USING TOEPLITZ MATRIX: ALGORITHM DESIGN AND FPGA IMPLEMENTATION", Acceptance rate (46.5%).
- [IEEE CCWC'19] Oscar Olazabal, Mikhail Gofman, Yu Bai, Yoonsuk Choi, Noel Sandico, Sinjini Mitra, and Kevin Pham, "Multimodal Biometrics for Enhanced IoT Security" 2019 IEEE 9th Annual Computing and Communication Workshop and Conference (CCWC), Las Vegas, NV, USA, 2019, Accepted(?)
- [IEEE CCWC'19] Rakshith Ravishankar, Yu Bai, Yoonsuk Choi, "Design and Implementation of 32-Channel ADPCM CODEC" 2019 IEEE 9th Annual Computing and Communication Workshop and Conference (CCWC), Las Vegas, NV, USA, 2019, Accepted(?)
- [ACM GLSVLSI'18] Shaahin Angizi, Zhezhi He, Yu Bai, Jie Han, Mingjie Lin and Deliang Fan, "Leveraging Spintronic Devices for Efficient Approximate Logic and Stochastic Neural Network," In Proceedings of the 2018 on Great Lakes Symposium on VLSI (GLSVLSI '18). ACM, New York, NY, USA, 397-402. DOI: https://doi.org/10.1145/3194554.3194618 (Invited) Acceptance rate (28.6%)
- [IEEE CCWC'18] D. G. Anil, Y. Bai and Y. Choi, "Performance evaluation of ternary computation in SRAM design using graphene nanoribbon field effect transistors," 2018 IEEE 8th Annual Computing and Communication Workshop and Conference (CCWC), Las Vegas, NV, USA, 2018, pp. 382-388. doi: 10.1109/CCWC.2018.8301723 Acceptance rate (?)
- [IEEE/ACM MICRO'17] Caiwen Ding, Siyu Liao, Yanzhi Wang, Zhe Li, Ning Liu, Youwei Zhuo, Chao Wang, Xuehai Qian, Yu Bai, Geng Yuan, Xiaolong Ma, Yipeng Zhang, Jian Tang, Qinru Qiu, Xue Lin, and Bo Yuan. 2017. CirCNN: accelerating and compressing deep neural networks using block-circulant weight matrices. In Proceedings of the 50th Annual IEEE/ACM International Symposium on Microarchitecture (MICRO-50 '17). ACM, New York, NY, USA, 395-408. DOI: https://doi.org/10.1145/3123939.3124552 Acceptance rate (18%)
- [IIKI'17] Yanhui Guo, Siming Han, Ying Li, Cuifen Zhang and Yu Bai*,"K-Nearest Neighbor combined with guided filter for hyperspectral image classfication" 2017 International Conference on Identification, Information and Knowledge in the Internet of Things (IIKI2017), 2018 Dec 31;129:159-65. Acceptance rate (31.2%)
- [IEEE MWSCAS'17] Azadeh Famili, Yoonsuk Choi, and Yu Bai*,"High Efficient Reconfigurable PUF Through Spin Hall-Induced Coupled-Oscillators" 2017 IEEE 60th International Midwest Symposium on Circuits and Systems (MWSCAS 2017),DOI: 10.1109/MWSCAS.2017.8053124, invited paper. Acceptance rate (36.3%)

- [ACM GLSVLSI'17] Yu Bai, Sharon Hu, Ronald F.DeMara, Mingjie Lin,"A Spin-Orbit Torque based Cellular Neural Net- work (CNN) Architecture" 2017 27rd ACM International Conference on Great lakes symposium on VLSI (GLSVLSI2017), 2017:59-64. Acceptance rate (24.4%)
- [IEEE IIKI'16] Y. Guo, L. Meng, X. Tang, Y. Shi, H. Cao and Y. Bai*, "Artificial Haze Immune Algorithm for Image Processing," 2016 International Conference on Identification, Information and Knowledge in the Internet of Things (IIKI), Beijing, 2016, pp. 93-98. doi: 10.1109/IIKI.2016.14 Acceptance rate (38%)
- [IEEE ICICM'16] Yu Bai, Mingjie Lin," Stochastic-Based Logic Circuit Synthesis and Implementation through Large-Fanin Threshold Logic with Magnetic Tunneling Junctions "2016 IEEE International Conference on Integrated Circuits and Microsystems (ICICM 2016), Chengdu, 2016, pp. 55-60. doi: 10.1109/ICAM.2016. 7813563. Acceptance rate (47%)
- [ACM GLSVLSI'16] Yu Bai, Hu B., Kuang W., Lin M.," Magnetic domain wall implemented null convention logic," 26rd ACM International Conference on Great lakes symposium on VLSI (GLSVLSI2016), DOI=http://dx.doi.org/10.1145/2902961.2903019. Acceptance rate (20.1%)
- [ACM FPGA'16] Yu Bai, Lin M.,"Stochastic-based spin-programmable gate array with emerging MTJ device technology (Abstract only)," In Proceedings of the 2016 ACM/SIGDA International Symposium on Field-Programmable Gate Arrays (FPGA16). Pages 279-279 ACM New York, NY, USA, ISBN: 978-1-4503-3856-1 doi>10.1145/2847263.2847317. Acceptance rate (24%)
- [IUCC'15] Yu Bai, Lin M.," Universal random number generation with field-programmable analog array," 14th 2015 IEEE International Conference on Ubiquitous Computing and Communications (IUCC2015), Liverpool, 2015, pp. 1338-1343.doi: 10.1109/CIT/IUCC/DASC/PICOM.2015.198. Acceptance rate (20.3%)
- [ACM FPGA'15] Yu Bai, Lin M.,"Energy-efficient discrete signal processing with field programmable analog arrays (FPAAs)," In Proceedings of the 2015 ACM/SIGDA International Symposium on Field-Programmable Gate Arrays (FPGA15). ACM, New York, NY, USA, 84-93. DOI=10.1145/2684746.2689078. Acceptance rate (24%)
- [IEEE ReConFig'14] Yu Bai, Lin M.,"Stochastically computing discrete Fourier transform with reconfigurable digital fabric,"ReConFigurable Computing and FPGAs (ReConFig), 2014 International Conference on , vol., no., pp.1,7, 8-10 Dec. 2014 doi: 10.1109/ReConFig.2014.7032558. Acceptance rate (29.3%)
- [ACM FPGA'14] Mohammed Alawad, Yu Bai, Ronald DeMara, Lin M.," Energy-efficient multiplier-less discrete convolver through probabilistic domain transformation," In Proceedings of the 2014 ACM/SIGDA Interna- tional Symposium on Field-programmable Gate Arrays (FPGA14). ACM, New York, NY, USA, 185-188. DOI=10.1145/2554688.2554769 http://doi.acm.org/10.1145/2554688.2554769. Acceptance rate (36%)
- [ACM FPGA'14] Yu Bai, Mohammed Alawad, and Mingjie Lin.,"Optimally mitigating BTI-induced FPGA device ageing with discriminative voltage scaling (abstract only), "In Proceedings of the 2014 ACM/SIGDA International Symposium on Field programmable Gate Arrays (FPGA14). ACM, New York, NY, USA, 246-246. DOI=10.1145/2554688.2554752 http://doi.acm.org/10.1145/2554688.2554752. Acceptance rate (36%)
- [IEEE ReConFig'13] Yu Bai, Alawad, M., Riera, M., Mingjie Lin, "Improving memory performance in reconfigurable computing architecture through hardware-assisted dynamic graph," Reconfigurable Computing and FPGAs (ReConFig), 2013 International Conference on , vol., no., pp.1,8, 9-11 Dec. 2013 doi: 10.1109/ReConFig.2013.6732300. Acceptance rate (35.2%)
- [IEEE FCCM'13] Yu Bai; Alawad, M.; Riera, M.; Mingjie Lin," Boosting memory performance of many core FPGA device through dynamic precedence graph," Field-Programmable Custom Computing Machines (FCCM), 2013 IEEE 21st Annual International Symposium on , vol., no., pp.21,24, 28-30 April 2013 doi: 10.1109/FCCM.2013. 39. Acceptance rate (28%)
- [IEEE ICAIEES'13] Mohammed Alawad, Yu Bai, and Mingjie Lin," Probabilistic domain transformation A robust and energy efficient computing means(regular paper)," 2013 International Conference on Advanced Information Engi- neering and Education Science (ICAIEES 2013). Acceptance rate (?)
- [ACM FPGA'13] Yu Bai , Abigail Fuentes, Mingjie Lin, Riera, M.,"Exploiting algorithmic-level memory parallelism in distributed logic memory architecture through hardware assisted dynamic graph (abstract only), "In Proceedings of the ACM/SIGDA international symposium on Field programmable gate arrays (FPGA 2013). ACM, New York, NY, USA, 273-273. DOI=10.1145/2435264.2435333 http://doi.acm.org/10.1145/2435264.2435333. Acceptance rate (24%)
- [IEEE HASE'11] Mingjie Lin, Yu Bai, Wawrzynek J., "Discriminatively fortified computing with reconfigurable digital fabric," High-Assurance Systems Engineering (HASE), 2011 IEEE 13th International Symposium on , vol., no., pp.112,119, 10-12 Nov. 2011 doi: 10.1109/HASE.2011.49. Acceptance rate (24%)

- [DSD'11] Yu Bai, Weidong Kuang, "Design of asynchronous circuits on FPGAs for soft error tolerance," Digital System Design (DSD), 2011 14th Euromicro Conference on , vol., no., pp.247,253, Aug. 31 2011-Sept. 2 2011 doi: 10.1109/DSD.2011.35. Acceptance rate (31%)
- [SPL'11] Weidong Kuang, Yu Bai, "Soft error in FPGA-implemented asynchronous circuits," Programmable Logic (SPL), 2011 VII Southern Conference on , vol., no., pp.221,226, 13-15 April 2011 doi: 10.1109/SPL.2011. 57826 52. Acceptance rate (?)

PROFESSIONAL ACTIVITIES

National Science Foundation

Computer Network System Panelist

Department of Defense

• Intelligent System Panelist

Journal Editorial Board

- Guest Editor of Journal Internet of Things, Elsevier (IF=5.7)
- Guest Editor of Journal of Low Power Electronics and Applications (IF=1.08)
- Guest Editor of Computer Network, Elsevier (IF=2.58)
- Guest Editor of Journal of Robotics and Mechatronics (SCI index),

Conference General Chair

 General Chair of International Conference on Identification, Information, and Knowledge in the Internet of Things (IIKI2022)

Conference Steering Committee

 IEEE International Symposium on Advanced Technologies and Applications in the Internet of Things (ATAIT 2018)

Conference Chair

- Publication Chair at International Conference on Field-Programmable Technology (FPT2020)
- Special Issue Chair at International Conference on Identification, Information, and Knowledge in the Internet of Things (IIKI2019,2020,2021)

Conference Track Chair

- Track Chair at International Symposium on Quality Electronic Design (ISQED'23)
- Tech Session Chair at ACM Great Lakes Symposium on VLSI (GLSVLSI) (GLSVLSI2020)
- Track Chair at International Conference on Identification, Information and Knowledge in the Internet of Things (IIKI2017,2018,2019,2020,2021,2022)

Conference Technical Program Committee

- Design Automation Conference (DAC2021,2022)
- International Conference on Field-Programmable Technology (FPT2019,2020)
- International Conference on Identification, Information and Knowledge in the Internet of Things (IIKI2017,2018,2019,2020)
- ACM edition of the Great Lakes Symposium on VLSI (GLSVLSI2019,2020,2021,2022)
- IEEE Computer Society Annual Symposium on VLSI (ISVLSI2017,2018,2019,2020,2021,2022)
- IEEE International Conference on Wireless Algorithms, Systems, and Application (WASA2018,2019,2020,2021)
- IEEE International Performance Computing and Communications Conference (IPCCC2018,2019,2020,2021,2022)
- IEEE International Conference on Big Data (IEEE BigData 2018)
- IEEE International Conference on Computer and Communication Engineering Technology (CCET 2018)
- IEEE International Conference on Sensors, Signal and Image Processing (SSIP2018)

- IEEE International Conference on Integrated Circuits and Microsystems(ICICM2016,2017,2018)
- IEEE International Conference on Smart City (SmartCity-2018)
- IEEE International Conference on Internet of Things (iThings 2017)
- IEEE International Conference on Computer and Communications (ICCC 2016)

Invited Reviewer

- IEEE Transactions on Emerging Topics in Computational Intelligence
- IEEE ACCESS
- Journal of Information Sciences
- Journal of Nanoelectronics and Optoelectronics, Elsevier
- Journal of Applied Sciences
- Journal of Sensors
- IEEE Transactions on Network Science and Engineering
- IEEE Communications Magazine
- IEEE Transaction on Computers
- IEEE Transaction on Multi-scale computing
- Journal of Nanoelectronics and Optoelectronics, Elsevier
- Journal of Applied Sciences
- Journal of Sensors
- Journal of Information Sciences, Elsevier
- Journal of Physica A
- Journal Integration, the VLSI Journal, Elsevier
- Journal of Ambient Intelligence and Humanized Computing, Springer
- PLOS ONE
- Journal of Network and Computer Applications, Springer
- Electronic Commerce Research, Springer
- Journal of Low Power Electronics and Applications
- Soft Computing, Springer
- Microelectronics Journal, Elsevier
- ACM/SIGDA International Symposium on Field-Programmable Gate Arrays (FPGA)
- International Conference on Reconfigurable Computing and FPGAs (ReConFig)